

SAW Components

Data Sheet B3825





Data Sheet

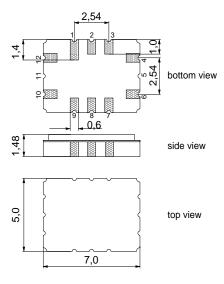
Ceramic package QCC12C

Features

- IF low-loss filter for base stations
- Channel selection in W-CDMA systems
- Balanced and unbalanced operation possible
- 3,84 MHz usable bandwidth
- Ceramic SMD package

Terminals

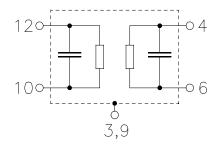
■ Gold plated



Dim. in mm, aprox. weight 0,22 g

Pin configuration

12	Input
10	Input ground or balanced input
6	Output
4	Output ground or balanced output
1, 2, 7, 8	to be grounded
3, 9	Case - ground



Туре	Ordering code	Marking and Package according to	Packing according to
B3825	B39381-B3825-H310	C61157-A7-A95	F61074-V8170-Z000

Electrostatic Sensitive Device (ESD)

Maximum ratings

Operable temperature range	T	- 40/+ 85	°C
Storage temperature range	$T_{\rm stg}$	- 40/+ 85	°C
DC voltage	$V_{\rm DC}$	0	V
Source power	P_{s}	10	dBm



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Characteristics (unbalanced operation)

Operating temperature: $T = -25 \text{ to } +85 \,^{\circ}\text{C}$ Terminating source impedance: $Z_{\text{S}} = 577 \,\Omega \parallel 20 \,\text{nH}$ Terminating load impedance: $Z_{\text{L}} = 817 \,\Omega \parallel 21 \,\text{nH}$

			min.	typ.	max.	
Nominal frequency		f_{N}	_	380,0	_	MHz
Minimum insertion attenuation (including matching network 1))		α_{min}	8,0	8,9	10,0	dB
Passband width	$\alpha_{rel} \leq 3,0 \text{ dB}$	B _{3,0dB}	4,9	5,1	5,3	MHz
Amplitude ripple (p-p)	f _N ± 1,92 MHz	Δα	0,2	1,0	1,2	dB
Phase ripple (p-p)	$f_{ m N}$ ± 1,92 MHz	Δφ	3,0	5,0	7,0	۰
Absolute group delay	@ f _N	τ	360	460	560	ns
Group delay ripple (p-p)	$f_{ m N}$ ± 1,92 MHz	Δτ	40	80	180	ns
Mean value of absolute group	delay $f_{\rm N} \pm 1,92~{\rm MHz}$	$\overline{ au}$	440	460	480	ns
Adjacent channel selectivity		ACS	24	32	39	dB
Intermodulation		IM3				
f1 = 360 MHz, input power 0 dBm f2 = 370 MHz, input power 0 dBm @ f_N f1 = 360 MHz, input power -5 dBm			-120	-95	-85	dBm
$f2 = 370 \text{ MHz}$, input power -5 dBm $@f_{\text{N}}$			-135	-110	-100	dBm



SAW Components B3825 380,00 MHz **Low-Loss Filter**

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	min.	typ.	max.	
f1 = 390 MHz, input power 0 dBm				
f2 = 400 MHz, input power 0 dBm				
$@f_{N}$	-120	-95	-85	dBm
€ I _N	120	30	00	GDIII
f1 = 390 MHz, input power -5 dBm				
f2 = 400 MHz, input power -5 dBm				
@f _N	-135	-110	-100	dBm
Minimum relative attenuation (relative to α_{min}) α_{rel}				
at f _N - 5,0 MHz	37	40	50	dB
at f _N + 5,0 MHz	40	45	50	dB
DC f_N - 20,0 MHz	42	46	55	dB
f _N - 20,0 MHz f _N - 17,5 MHz	35	38	45	dB
f _N - 17,5 MHz f _N - 13,5 MHz	42	45	55	dB
$f_{\rm N}$ - 13,5 MHz $f_{\rm N}$ - 7,5 MHz	38	40	45	dB
f _N - 7,5 MHz f _N - 4,1 MHz	35	38	45	dB
$f_{\rm N}$ - 4,1 MHz $f_{\rm N}$ - 3,2 MHz	20	22	40	dB
$f_{\rm N}$ + 3,2 MHz $f_{\rm N}$ + 4,1 MHz	20	23	40	dB
$f_{\rm N}$ + 4,1 MHz $f_{\rm N}$ + 5,0 MHz	34	37	45	dB
$f_{\rm N}$ + 5,0 MHz $f_{\rm N}$ + 8,0 MHz	37	39	45	dB
$f_{\rm N}$ + 8,0 MHz $f_{\rm N}$ + 10,5 MHz	32	35	45	dB
$f_{\rm N}$ + 10,5 MHz $f_{\rm N}$ + 17,5 MHz	39	42	50	dB
$f_{\rm N}$ + 17,5 MHz $f_{\rm N}$ + 20,0 MHz	35	38	45	dB
$f_{\rm N}$ + 20,0 MHz $f_{\rm N}$ +100,0 MHz	40	43	55	dB
Impedance at f _N (without matching)				
Input: $Z_{IN} = R_{IN} C_{IN}$	_	795 6	_	$\Omega \parallel pF$
Output: $Z_{OUT} = R_{OUT} C_{OUT}$	_	652 6	<u> </u>	Ω pF
Temperature coefficient of frequency 2) TC _f	_	-0,036		ppm/K ²
Turnover temperature T_0	_	25	_	°C

¹⁾ Matching inductor Q=40

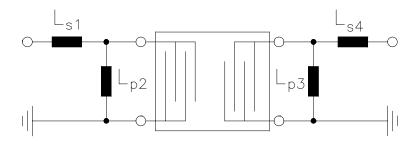
 $^{^{2)}}$ Temperature dependance of $f_{\rm c}$: $f_{\rm c}(T_{\rm A})$ = $f_{\rm c}(T_0)(1$ + $TC_{\rm f}(T_{\rm A}-T_0)^2)$



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Matching network

(Element values depend upon PCB layout)



$$L_{s1} = 68 \text{ nH}$$

 $L_{p2} = 27 \text{ nH}$

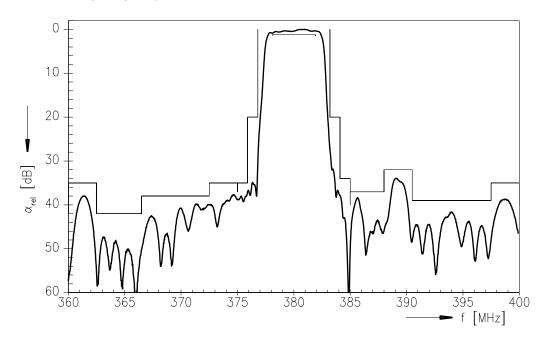
$$L_{p3} = 27 \text{ nH}$$

 $L_{s4} = 82 \text{ nH}$

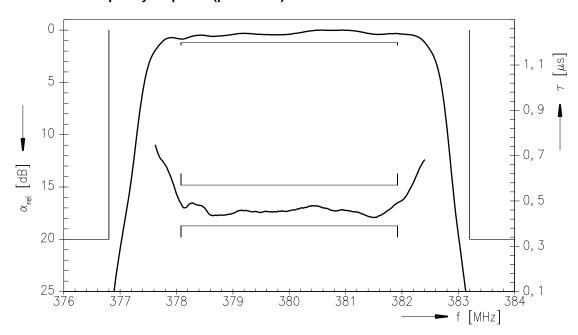


Data Sheet

Normalized frequency response



Normalized frequency response (pass band)





Data Sheet

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