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2N1777A

Silicon Controlled Rectifier

MAXIMUM ALLOWABLE RATINGS

TYPE	PEAK FORWARD BLOCKING VOLTAGE, V_{FOM} $T_a = -65^\circ C$ to $+150^\circ C$	REPETITIVE PEAK REVERSE VOLTAGE, $V_{ROM} (\text{rep})^{**}$ $T_a = -65^\circ C$ to $+150^\circ C$	NON-REPETITIVE PEAK REVERSE VOLTAGE (<5.0 MILLISEC.), $V_{ROM} (\text{non-rep})^{**}$ $T_a = -65^\circ C$ to $+150^\circ C$
(2N1777A)	400 volts*	400 Volts*	500 Volts*

**Values apply for zero or negative gate voltage only. Maximum case to ambient thermal resistance for which maximum V_{FOM} and V_{ROM} ratings apply = $18^\circ C$ per watt.

Peak Forward Voltage, PFV	480 volts
RMS Forward Current, On-State	7.4 amperes (all conduction angles)
Average Forward Current, On-State, Half Sine Wave, I_{av}	4.7 amperes at $T_a = 105^\circ C$ *
Average Forward Current, On-State	Depends on conduction angle (see Chart 3, 5 and 7)
Peak One-cycle Surge Forward Current, I_{FSI} (surge)	60 amperes*
I^2t (for fusing)	Calculate from Chart 9
Turn-On Current Limit	See Chart 10
Peak Gate Power Dissipation, P_{GOM}	5 watts*
Average Gate Power Dissipation, $P_{G} (\text{AV})$	0.5 watts*
** Peak Gate Current, I_{GFM}	2 amperes*
** Peak Gate Voltage, Forward and Reverse, V_{GFM} and V_{GRM}	10 volts*
Storage Temperature, T_{stg}	$-65^\circ C$ to $+150^\circ C$ *
Operating Temperature, T_a	$-65^\circ C$ to $+150^\circ C$ *
Stud Torque	15 lb-in (17 kg-cm)

*Indicates data included on JEDEC type number registration.

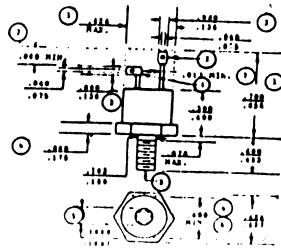
**NOT TO EXCEED GATE POWER RATINGS

CHARACTERISTICS

TEST	SYMBOL	MIN.	MAX.	UNITS	TEST CONDITIONS
PEAK REVERSE OR FORWARD BLOCKING CURRENT† C10D(2N1777A)	I_{ROM} or I_{FOM}	—	2.0	mA	$T_a = -65^\circ C$ to $+150^\circ C$ $V_{ROM} = V_{FOM} = 400V$ Peak
FULL CYCLE AVG. REVERSE OR FORWARD BLOCKING CURRENT† C10D(2N1777A)	$I_{RX \text{ AVE}}$ or $I_{FX \text{ AVE}}$	—	1.0*	mA	$T_a = -105^\circ C$, $I_a = 4.7A$ 180° Conduction Angle $V_{ROM} = V_{FOM} = 400V$ Peak
GATE TRIGGER CURRENT	I_{GT}	—	15	mA dc	$T_a = +25^\circ C$, $V_{GT} = 12$ Vdc, $R_L = 250$ ohms
		—	30*	mA dc	$T_a = -65^\circ C$, $V_{GT} = 12$ Vdc, $R_L = 250$ ohms
GATE TRIGGER VOLTAGE	V_{GT}	—	2.0*	Vdc	$T_a = -65^\circ C$ to $+150^\circ C$, $V_{GT} = 12$ Vdc, $R_L = 250$ ohms
		—	0.2*	Vdc	$T_a = +150^\circ C$, $V_{GT} = \text{Rated } V_{ROM}$, $R_L = 250$ ohms
PEAK ON-VOLTAGE	V_{FM}	—	1.85	V	$T_a = +25^\circ C$, $I_{av} = 15A$ Peak, 1 millisecond wide pulse. Duty cycle < 1%.
HOLDING CURRENT	I_{HOL}	—	25	mA dc	$T_a = +25^\circ C$, Anode supply = 24 Vdc, Gate Supply = 7V, 20 ohms. Initial forward current pulse $\approx 0.5A$, 0.1 millisecond to 10 milliseconds wide.
EFFECTIVE THERMAL RESISTANCE (DC)	θ_{JC}	—	3.1	°C/watt	Junction to case.

†Values apply for zero or negative gate voltage only. Maximum case to ambient thermal resistance for which maximum V_{FOM} and V_{ROM} ratings apply equals $18^\circ C$ /watt.

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NOTES

- The outline contour with exception of hexagon is optional within some or dimension specified.
- Minimum difference in terminal lengths to establish datum line for numbering terminals.
- Contour and orientation of fixed terminal lugs are optional.
- A chamfer (or undercut) on one or both ends of hexagonal portion is optional.
- Pitch diameter - thread 10-32 UNF-2A (coated). Reference (screw thread standards for federal services - handhook H-7B).
- Minimum diameter of seating plane.
- Minimum spacing between terminals.

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