



SBE812

Schottky Barrier Diode

60V, 1A, Low IR, Non-Monolithic Dual VEC8 Common Cathode

ON Semiconductor®

<http://onsemi.com>

Applications

- High frequency rectification (switching regulators, converters, choppers)

Features

- Small switching noise
- Low leakage current and high reliability due to highly reliable planar structure
- Ultrasmall package allows applied sets to be made small and thin

Specifications

Absolute Maximum Ratings at Ta=25°C (Value per element)

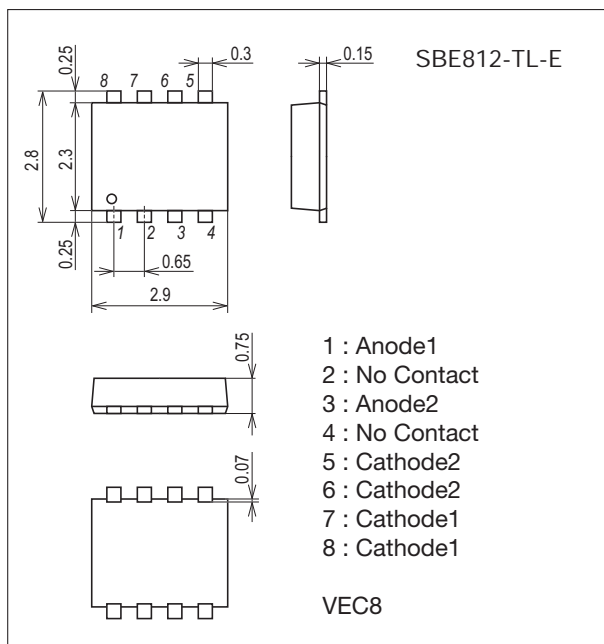
Parameter	Symbol	Conditions	Ratings	Unit
Repetitive Peak Reverse Voltage	V _{RRM}		60	V
Nonrepetitive Peak Reverse Surge Voltage	V _{RSM}		65	V
Average Output Current	I _O		1.0	A
Surge Forward Current	I _{FSM}	50Hz sine wave, 1 cycle	10	A
Junction Temperature	T _j		-55 to +125	°C
Storage Temperature	T _{stg}		-55 to +125	°C

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

Package Dimensions

unit : mm (typ)

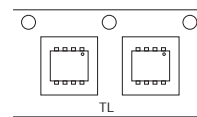
7012-001



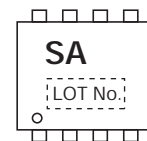
Product & Package Information

- Package : VEC8
- JEITA, JEDEC : -
- Minimum Packing Quantity : 3,000 pcs./reel

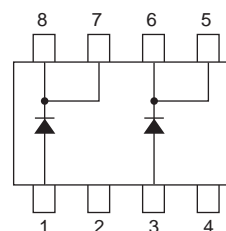
Packing Type : TL



Marking



Electrical Connection

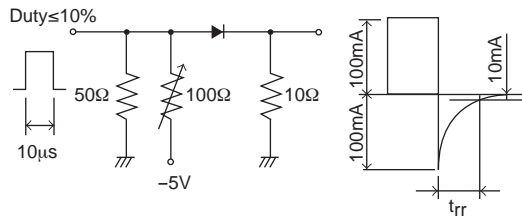


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Electrical Characteristics at Ta=25°C (Value per element)

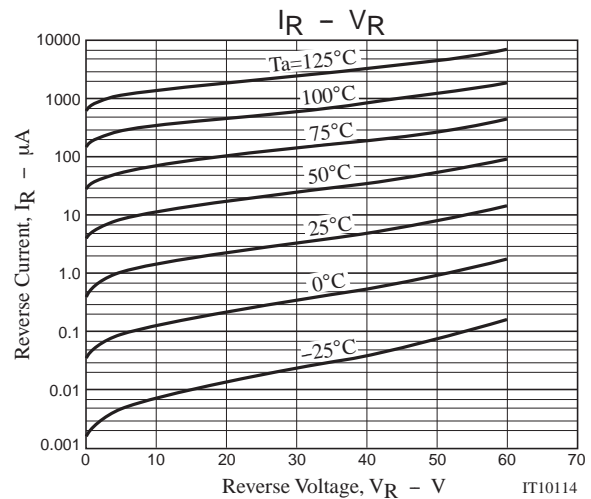
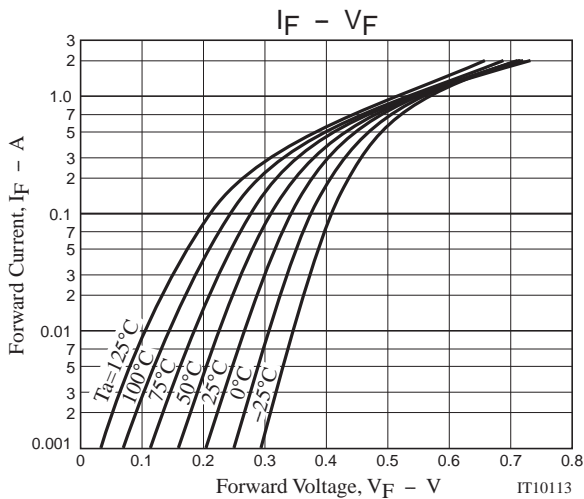
Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	
Reverse Voltage	V _R	I _R =0.2mA	60			V
Forward Voltage	V _F	I _F =1A		0.55	0.60	V
Reverse Current	I _R	V _R =30V			30	μA
Interterminal Capacitance	C	V _R =10V, f=1MHz		35		pF
Reverse Recovery Time	t _{rr}	I _F =100mA, See specified Test Circuit.			10	ns
Thermal Resistance	Rth(j-a)1	When Mounted in Cu-foiled area of 1.92mm ² ×0.03mm on glass epoxy substrate		80		°C / W
	Rth(j-a)2	When mounted on ceramic substrate (1000mm ² ×0.8mm)		75		°C / W

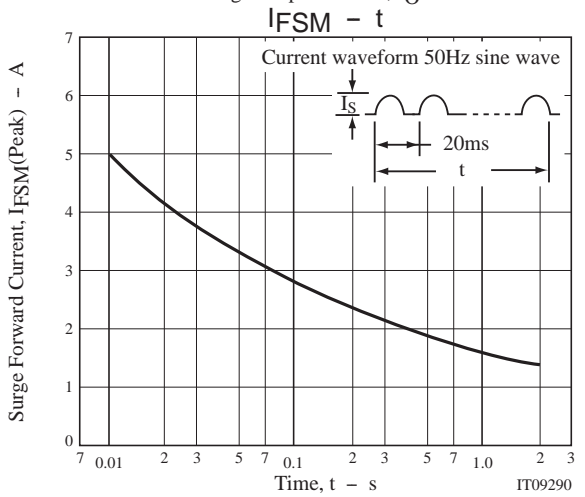
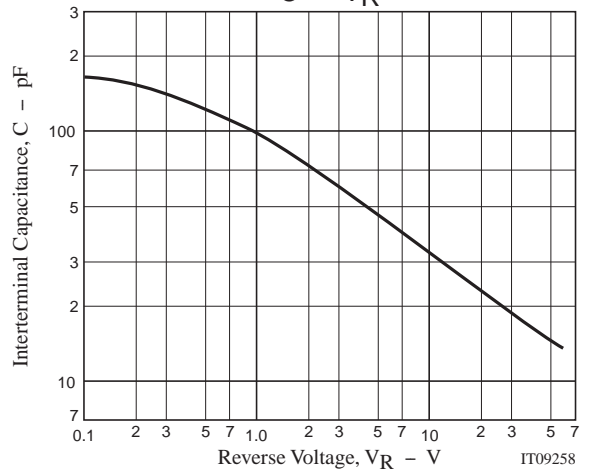
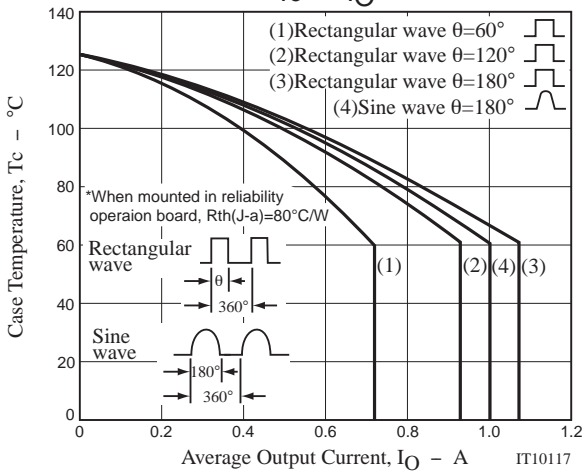
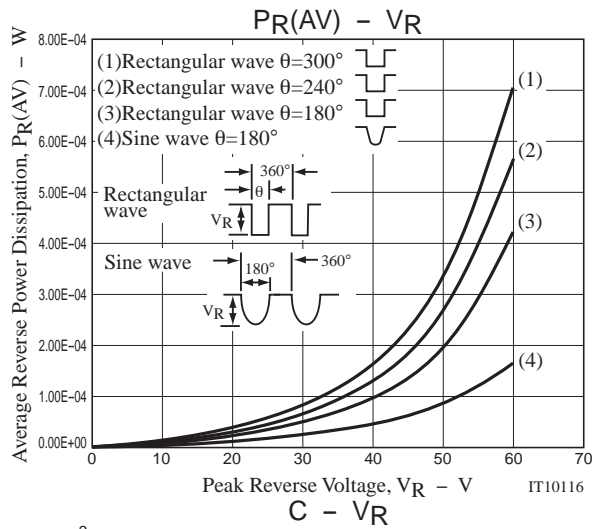
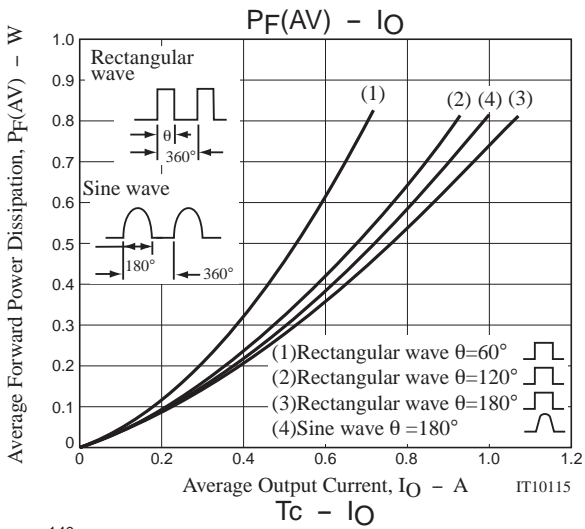
t_{rr} Test Circuit



Ordering Information

Device	Package	Shipping	memo
SBE812-TL-E	VEC8	3,000pcs./reel	Pb Free





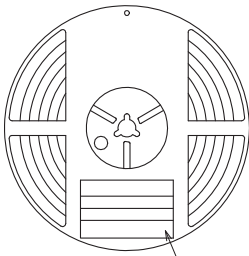
Taping Specification

SBE812-TL-E

1. Packing Format

Package Name	Carrier Tape Type	Maximum Number of devices contained (pcs)			Packing format	
		Reel	Inner box	Outer box	Inner BOX (C-1) Dimensions:mm (external) 183×72×185	Outer BOX (A-7) Dimensions:mm (external) 440×195×210
VEC8	CPH6	3,000	15,000	90,000	5 reels contained	6 inner boxes contained

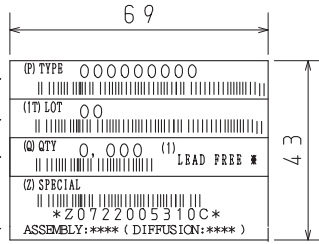
Packing method



Reel label

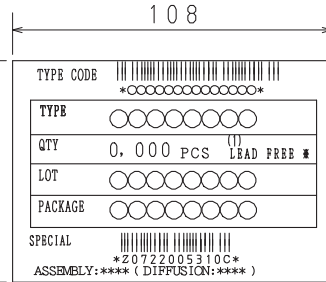
Type No.
LOT No.
Quantity
Origin

Reel label, Inner box label
(unit:mm)



Outer box label

It is a label at the time of factory shipments. The form of a label may change in physical distribution process.



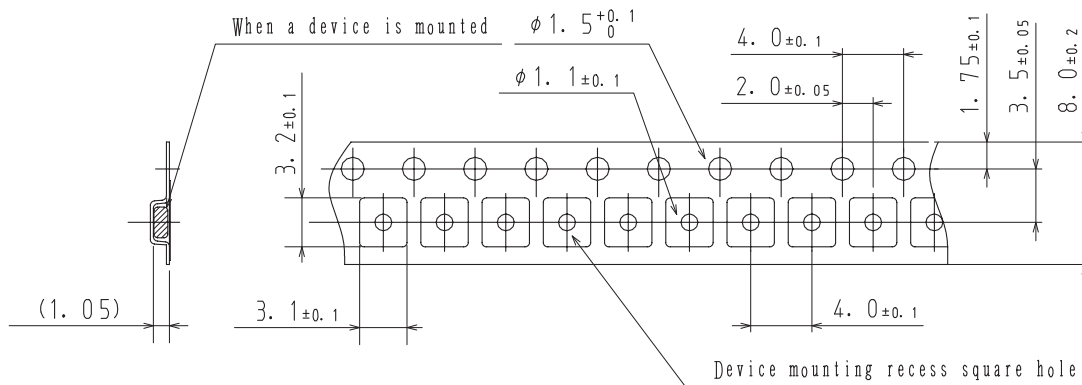
NOTE (1)

The LEAD FREE * description shows that the surface treatment of the terminal is lead free.

Label	JEITA Phase
LEAD FREE 3	JEITA Phase 3A
LEAD FREE 4	JEITA Phase 3

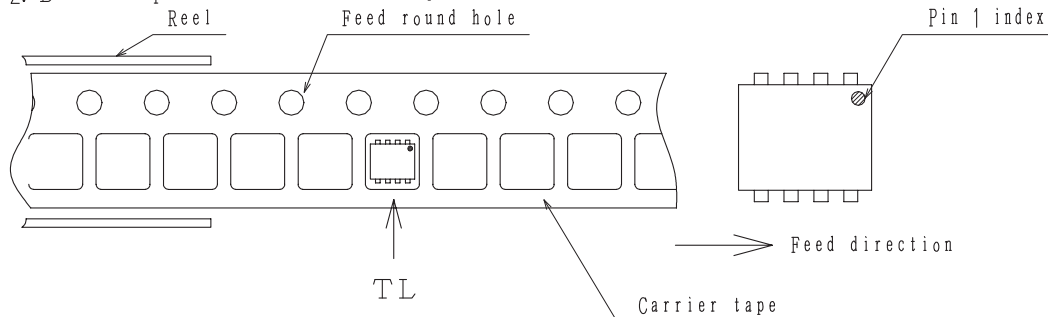
2. Taping configuration

2-1. Carrier tape size (unit:mm)



Device mounting recess square hole

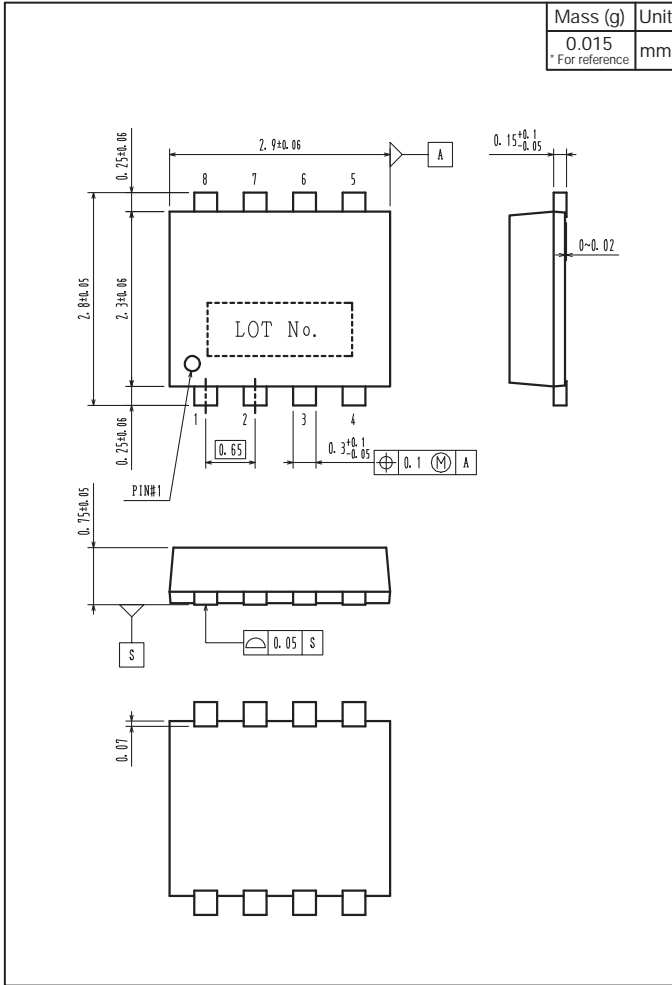
2-2. Device placement direction



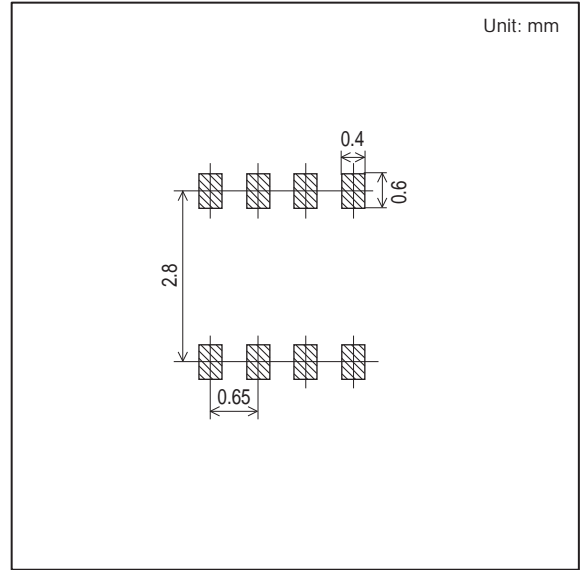
Those with oen electrode terminal on the feed hole side.....TL

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Outline Drawing SBE812-TL-E



Land Pattern Example



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